

 <p>INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)</p>				ATTY DOCKET NO. 02008/092002		APPLICATION NO. 10/776,926	
				APPLICANT(S) T. YAMAGUCHI, et al.			
				FILING DATE 02/11/2004		GROUP ART UNIT	
				U.S. PATENT DOCUMENTS			
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
/E.B./	A1	5,418,789	05/23/1995	J.e. Gersbach, et al	371	5.2	10/14/1992
U.S. PATENT APPLICATION PUBLICATIONS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
/E.B./	B1	Digital Line Systems Based on the Synchronous Digital hierarchy for Use on Optical Fibre Cables, ITU-T Recommendation G-958, dated November 1994, 26 pages					
/E.B./	B2	Jitter and wander measuring equipment for digital systems which are based on the synchronous digital hierarchy (SDH), ITU-T Recommendation 0.172, dated March 1999, 38 pages					
EXAMINER /Emmanuel Bayard/				DATE CONSIDERED 12/17/2007			
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/E.B./	B3	SONET OC-192 Transport system Generic Criteria, GR-1377-CORE, Issue 5, dated December 1998, 104 pages					
/E.B./	B4	The control of jitter and wander within digital networks which are based on the synchronous digital hierarchy (SDH) ITU-T Recommendation G.825, dated March 2000, 22 pages					
EXAMINER				DATE CONSIDERED			
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/E.B./	BS	E.A. Lee and D.G. Messerschmitt, Digital Communication, 2nd Edition, 5 pages, 1994					
/E.B./	B6	International Search Report, dated October 1, 2003, 3 pages					
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)								
/E.B./		B7	Miller, C.M. et al., "Jitter Analysis of High-Speed Digital Systems", Hewlett-Packard Journal, Hewlett-Packard Co., Palo Alto, US, vol. 46, no 1, 1 February 1995 (1995-02-01), pages 49-56					
/E.B./		B8	Yamaguchi, T.J. et al., "Extraction of peak-to-peak and RMS sinusoidal jitter using an analytic signal method"; VLSI TestSymposium 2000. Proceedings. 18th IEEE Montreal, Que., Canada 30 April-4 May 2000, Los Alamitos, CA, USA, IEEE Comput. SOC, US 30 April 2000, pages 395-402					
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
/E.B./		B9	Baldini J.J. et al. "Jitter in digital transmission systems - characteristics and measurement techniques" Global Telecommunications Conference. Miami, Nov. 29-Dec. 2, 1982, New York, I.E.E.E., US vol 2 conf. 1982, November 1982 (1982-11) pages 658-664				
/E.B./		B10	Yamaguchi, T.J. et al. "Jitter measurements of a power-pc microprocessor using an analytic signal method", International Test Conference 2000, October 3-5, 2000 5 pages				
EXAMINER /Emmanuel Bayard/				DATE CONSIDERED 12/17/2007			
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10776926
Filing Date	2004-02-11
First Named Inventor	Takahiro Yamaguchi
Art Unit	2611
Examiner Name	Emmanuel Baynard
Attorney Docket Number	02008/092002

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/E.B./	1	7012982		2006-03-14	Basch et al.	

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First Named Inventor	Takahiro Yamaguchi
Art Unit	2611
Examiner Name	Emmanuel Baynard
Attorney Docket Number	02008/092002

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/776,926-Conf. #2381
				Filing Date	February 11, 2004
				First Named Inventor	Takahiro Yamaguchi
				Art Unit	2631
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	02008/092002

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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
/E.B./	BA	JP-8-50156 A	02/20/1996	Anritsu Corp.		
/E.B./	BB	JP-11-83924 A	03/26/1999	Hitachi Cable, Ltd.		
/E.B./	BC	JP-8-248078 A	09/27/1996	Anritsu Corp.		

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/E.B./	CA	International Search Report dated 05/20/2003 (1 page)	

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